

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.

Serial No. : 10/069,229

Filed : December 11, 2002

Art Unit : 2154

Examiner : Joshua Joo

Confirmation No. 1429

Title : BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE

**MAIL STOP AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

*Entered  
JJ*

REPLY TO FINAL ACTION OF MARCH 17, 2006

*5/15/06*

Please amend the above-identified application as follows: